

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	4	(co\$simulation) and (concurrent adj process\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 09:50
L3	518	(C++) and (RTL vhdL HDL) and library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:50
L4	1	(hybrid adj simulation adj architecture)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 09:50
L5	139	L3 and (concurrent\$4) and (sequential)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:51
L6	44	L5 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:51